

```

103 save and add abcd back 0 org cr
2301 , 6745 , ab89 , efcd , cr
dcfe , 98ba , 5476 , 1032 , cr
put 008 dup dup or a! 3 for indent
      left b! @+ !b up b! @+ !b next ; cr
sum 012 dup dup or a! 3 for indent
      left b! @b @ . + dup ffff and !+ indent
      up b! @b @ . + over 2* indent
      -if drop 1 . + dup then drop indent
      ffff and !+ next break);
sums 025 left a! @p . / msg / ! put indent
      left a! 63 for @b ! unnext sum ; cr
02f / r--- boot

```

```

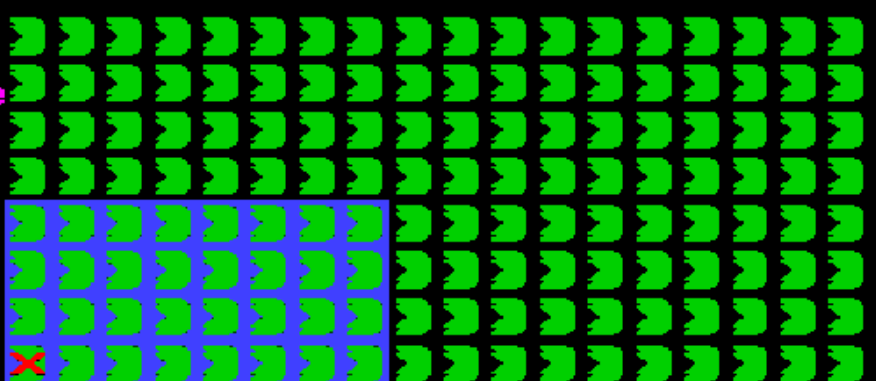
s o t yrg*
cdfj ludr
ab k -mct+
edit x.i

```

```

00
3000aa 301all 302all 303all 304all 305all 306all 307all
4 fetch 4 fetch 4 fetch 4 fetch 4 fetch 4 fetch 4 fetch
15555 15555 15555 15555 15555 15555 15555 15555
2 0aa 1 all 1 all 1 all 1 all 1 all 1 all
15555 e 15555 e 15555 e 15555 e 15555 e 15555 e
io io io io io io io io
15555 15555 15555 15555 15555 15555 15555 15555
15555 15555 15555 15555 15555 15555 15555 15555
15555 15555 15555 15555 15555 15555 15555 15555
e e e e e e e e
200rdu 201all 202u 203r 204l 205r 206l 207r
4 fetch 4 fetch 4 fetch 4 fetch 4 fetch 4 fetch 4 fetch
15555 15555 15555 15555 15555 15555 15555 15555
1 rdu 1 all 1 u 1 r 1 l 1 r 1 l 1 r
15555 e 15555 e 15555 15555 e 15555 e 15555 e 15555 e
io io io io io io io io
15555 15555 15555 15555 15555 15555 15555 15555
15555 15555 15555 15555 15555 15555 15555 15555
15555 15555 15555 15555 15555 15555 15555 15555
e e e e e e e e
100rdu 101all 102d 103r 104l 105r 106l 107r
4 fetch 4 fetch 4 fetch 4 fetch 4 fetch 4 fetch 4 fetch
15555 15555 15555 15555 15555 15555 15555 15555
1 rdu 1 all 1 d 1 r 1 l 1 r 1 l 1 r
15555 e 15555 e 15555 15555 e 15555 e 15555 e 15555 e
io io io io io io io io
15555 15555 15555 15555 15555 15555 15555 15555
15555 15555 15555 15555 15555 15555 15555 15555
15555 15555 15555 15555 15555 15555 15555 15555
e e e e e e e e
000rd 0010aa 002031 003030 004rdl 005rdl 006rdl 007rdl
4 fetch 4 fetch 4 fetch 4 fetch 4 fetch 4 fetch 4 fetch
15555 15555 15555 15555 15555 15555 15555 15555
1 rd 2 0aa 2 031 2 030 1 rdl 1 rdl 1 rdl 1 rdl
15555 e 15555 15555 15555 e 15555 e 15555 e 15555 e
io io io io io io io io
15555 15555 15555 15555 15555 15555 15555 15555
15555 15555 15555 15555 15555 15555 15555 15555
15555 15555 15555 15555 15555 15555 15555 15555
15555 15555 15555 15555 15555 15555 15555 15555
00 00000

```



```

000 15555
io
0f 134a9 call 0a9
0e 134a9 call 0a9
0d 134a9 call 0a9
0c 134a9 call 0a9
0b 134a9 call 0a9
0a 134a9 call 0a9
09 134a9 call 0a9
08 134a9 call 0a9
07 134a9 call 0a9
06 134a9 call 0a9
05 134a9 call 0a9
04 134a9 call 0a9
03 134a9 call 0a9
02 134a9 call 0a9
01 134a9 call 0a9
00 134a9 call 0a9

```

```

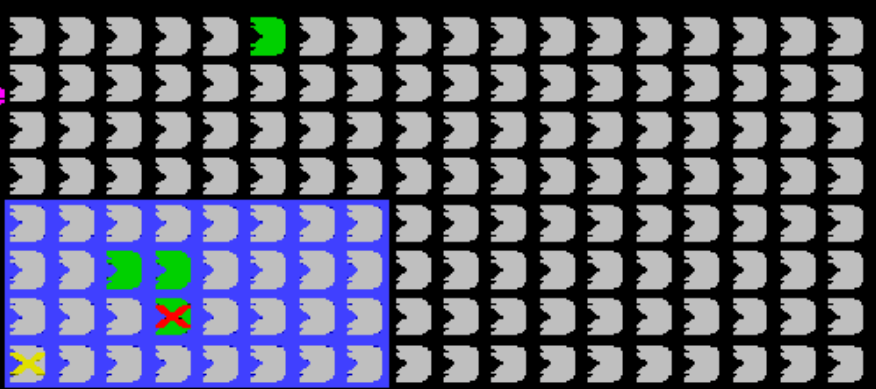
0
1
tw-+ l u d r
p f g s l u d r
sn-+ o h l w
so .

```

```

00e e e e e e e e
300all 301all 302all 303all 304all 305all 306all 307all
2e 4fetch 4fetch 4fetch 4fetch 4fetch 4fetch 4fetch
04a0a - 15555 - 15555 - 15555 - 15555 - 15555 - 15555
-0ac -all -all -all -all -all -all -all
e031a5 ee 15555 ee 15555 ee 15555 ee 15555 ee 15555 ee 15555 ee
io io io io io io io io
15555 15555 15555 15555 15555 15555 15555 15555
15555 15555 15555 15555 15555 15555 15555 15555
15555 15555 15555 15555 15555 15555 15555 15555
e e e e e e e e
200rdu 201all 202011 203r 204l 205r 206l 207r
4fetch 4fetch 2unext 0ep 3; 3; 0; 3;
15555 15555 07b72 05b72 01a75 07b75 0; 15555
-rdu -all 0 011 1 r 039 07b75 - 00b - 014
15555 ee 15555 e 00005 e 15555 e 15555 e 15555 e 15555 e
io io r r r r u io
15555 15555 15555 15555 15555 15555 15555 15555
15555 15555 00004 00014 15575 155d5 15575 15555
15555 15555 15555 0003f 15555 15555 15555 15555
15555 15555 15555 0003e 15555 15555 15555 15555
e e e e e e e e
0 e e e e e e e
100rdu 101all 102r 103r 104l 105r 106l 107r
4fetch 4fetch 1!b 0; 3; 3; 0;
15555 15555 07b72 15555 01a75 07b75 15555
-rdu -all - 01b - 039 - 00b - 014
15555 ee 15555 e 00001! e 00008 e 15555 e 15555 e 15555 e
io io r r r r u io
15555 15555 15555 15555 15555 15555 15555 15555
15555 15555 00008 155d5 15575 155d5 15575 15555
15555 15555 04b12 0932a 15555 15555 15555 15555
15555 15555 15555 1642f 15555 15555 15555 15555
e e e e e e e e
000rd 001all 002d 003rdl 004rdl 005rdl 006rdl 007rdl
4fetch 0call 0!b 0jump 4fetch 4fetch 4fetch 4fetch
15555 135a5 09017 115b5 15555 15555 15555 15555
-rd -0af - 019 - 0aa - rdl - rdl - rdl
15555 ee 03141 e 00000 e 00020 e 15555 ee 15555 ee 15555 ee
io io d r r r io io
15555 15555 15555 15555 15555 15555 15555 15555
15555 15555 00037 15555 15555 15555 15555 15555
15555 3+ffe 00000 00039 15555 15555 15555 15555
15555 15555 15555 00037 15555 15555 15555 15555
00e

```



```

103 00008
u
2f 134a9 call 0a9
2e 11412 jump 012
2d 01a72 eb ! unext .
2c 2e9b2 push . . .
2b 0003f eb and !+ ep
2a 00175 eb + unext ;
29 04a12 ep at ep .
28 0b608 ! call 08
27 1341c call 01c
26 00175 eb + unext ;
25 04a12 ep at ep .
24 15555 :
23 0f015 !+ next 15
22 0fff f + 0b and ep
21 3bdfa drop ep and .
20 249b2 dup . . .

```

```

27211
100
tw-+ l udr
pfgs l udr
sn-+ ohlw
so .

```

g18 software simulator compile empty br

prelude 1250 load cr
engine 1252 8 loads cr
opcodes 1268 4 loads cr
display 1240 load 1276 8 loads cr
keyboard 1292 2 loads cr
ports and pins 1296 2 loads br

connect ports cr
cfsoft 1248 load initialize pins and testbeds
cr
start power ok h exit

s o t yrg*
cdfj ludr
ab k -mct
edit x.i